

Freeform Search

<input type="checkbox"/> US Pre-Grant Publication Full-Text Database <input type="checkbox"/> US Patents Full-Text Database <input checked="" type="checkbox"/> US OCR Full-Text Database Database: <input type="checkbox"/> EPO Abstracts Database <input type="checkbox"/> JPO Abstracts Database <input type="checkbox"/> Derwent World Patents Index <input type="checkbox"/> IBM Technical Disclosure Bulletins	
4580246.uref. <input type="checkbox"/> <input checked="" type="checkbox"/> Term: <input type="checkbox"/>	
Display: <input type="text" value="10"/> Documents in <u>Display Format:</u> <input type="text"/> Starting with Number <input type="text" value="1"/>	
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

Search History

DATE: Saturday, April 24, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u>	<u>Hit</u>	<u>Set</u>
<u>Name</u>	<u>Count</u>	<u>Name</u>
side by side		result set
DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
<u>L8</u> 4580246.uref.	13	<u>L8</u>
<u>L7</u> write adj4 (protect or protected or protection) adj4 (control adj2 register)	20	<u>L7</u>
<u>L6</u> write adj4 (protect or protected or protection) adj4 register	187	<u>L6</u>
<u>L5</u> L4 and l2 and l1	37	<u>L5</u>
<u>L4</u> L3 same (initialization or BIOS or (power adj on))	4092	<u>L4</u>
<u>L3</u> (locked or protect or protected)	1373812	<u>L3</u>
<u>L2</u> reset same register same (clear or unlock or unprotected or unprotect)	6795	<u>L2</u>
<u>L1</u> register same (protect or protection or protected) same (sensitive or thermal or temperature)	456	<u>L1</u>

END OF SEARCH HISTORY

Freeform Search

Database:	<input type="checkbox"/> US Pre-Grant Publication Full-Text Database <input type="checkbox"/> US Patents Full-Text Database <input checked="" type="checkbox"/> US OCR Full-Text Database <input type="checkbox"/> EPO Abstracts Database <input type="checkbox"/> JPO Abstracts Database <input type="checkbox"/> Derwent World Patents Index <input type="checkbox"/> IBM Technical Disclosure Bulletins
Term:	<input type="text" value="4580246.uref."/>
Display:	<input type="text" value="10"/> Documents in <u>Display Format:</u> <input type="text"/> Starting with Number <input type="text" value="1"/>
Generate:	<input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image

Search **Clear** **Interrupt**

Search History

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<u>Set</u>	<u>Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set</u>
	side by side			result set
	<u>L8</u>	DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	13	<u>L8</u>
	<u>L7</u>	4580246.uref.	20	<u>L7</u>
	<u>L6</u>	write adj4 (protect or protected or protection) adj4 (control adj2 register)	187	<u>L6</u>
	<u>L5</u>	write adj4 (protect or protected or protection) adj4 register	37	<u>L5</u>
	<u>L4</u>	l4 and l2 and l1	4092	<u>L4</u>
	<u>L3</u>	L3 same (initialization or BIOS or (power adj on))	1373812	<u>L3</u>
	<u>L2</u>	(locked or protect or protected)	6795	<u>L2</u>
	<u>L1</u>	reset same register same (clear or unlock or unprotected or unprotect)	456	<u>L1</u>
		register same (protect or protection or protected) same (sensitive or thermal or temperature)		

END OF SEARCH HISTORY

First Hit

L7: Entry 19 of 20

File: DWPI

Sep 13, 1996

DERWENT-ACC-NO: 1996-468755

DERWENT-WEEK: 199647

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TITLE: Microcomputer with write protection function for various built in control for registers - controls write in operation of specific control register according to information held by protection control register

PATENT-ASSIGNEE: NEC KYUSHU LTD (KYUN)

PRIORITY-DATA: 1995JP-0040220 (February 28, 1995)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
<input type="checkbox"/> <u>JP 08235073 A</u>	September 13, 1996		009	G06F012/14

APPLICATION-DATA:

PUB-NO	APPL-DATE	APPL-NO	DESCRIPTOR
JP 08235073A	February 28, 1995	1995JP-0040220	

INT-CL (IPC): G06 F 12/06; G06 F 12/14

ABSTRACTED-PUB-NO: JP 08235073A

BASIC-ABSTRACT:

The microcomputer has number of control registers (20,30, 40) which are built in respective peripheral circuit (202-205). A protection control register (10) holds an information on whether each of the control register is permitted or forbidden for write in operation of data.

A specific distinction part specifies one of the control register which serves as a object for write out, when write in operation is permitted based on the information held by the protection control register. A control circuit (1) controls write in operation of specific control register according to the information on the protection control register.

ADVANTAGE - Prevents incorrect write in operation. Secures optimum design according to application. Reduces hardware requirement. Avoids need of special control signal such as SPCWR.

ABSTRACTED-PUB-NO: JP 08235073A

EQUIVALENT-ABSTRACTS:

CHOSEN-DRAWING: Dwg.1/9

h e b b g e e e f c e c hg

e ge

DERWENT-CLASS: T01
EPI-CODES: T01-H01C; T01-M01;

h e b b g e e e f c e c hg

e ge